IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Tae-sung Jung et al.
Serial No.: To Be Assigned
Filed: Concurrently Herewith

For: MEMORY INTERFA

MEMORY INTERFACE SYSTEMS THAT COUPLE A MEMORY TO A MEMORY CONTROLLER AND ARE RESPONSIVE TO A TERMINAL VOLTAGE THAT IS INDEPENDENT OF SUPPLY VOLTAGES FOR THE

MEMORY AND THE MEMORY CONTROLLER

May 8, 2001

Commissioner for Patents Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT CITATION UNDER 37 C.F.R. § 1.97

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP.

Respectfully submitted,

D. Scott Moore

Registration No. 42,011

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Audra Wooten

Date of Signature: May 8, 2001

FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office					Attorney Docket Number: 5649-894			Serial No.: To Be Assigned
LIST OF DOCUMENTS CITED BY APPLICANT								
(Use several sheets if necessary)						<u> </u>		
					Applicants: Tae-sung Jung et al.			
					Filing Date: Concurrently Herewith			Group: Unknown
U. S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name Class		Class	Subclass	Filing Date if Appropriate
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Со	untry	Class	Subclass	Translation Yes No
	1	JP 4281542	10-07-92	Japan				X /Abstract
	2	JP 4061690	02-27-92	Japan				X/Abstract
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
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